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10	Telephone: (619) 231-8666	JUL 2 4 2012	
12	Attorneys for Plaintiff	CLERK, U.S. DISTRICT COURT NORTHERN DISTRICT OF CALLERNIA	
13	13 UNITED STATES DISTRICT COURT		
14	4 NORTHERN DISTRICT OF CALIFORNIA HRL		
- 1:	TECHNOLOGY PROPERTIES LIMITED LLC, PHOENIX DIGITAL SOLUTIONS	CaseGo.V 12-03881	
10	5 LLC, and PATRIOT SCIENTIFIC CORPORATION,	COMPLAINT FOR PATENT INFRINGEMENT	
11	Plaintiffs,	DEMAND FOR JURY TRIAL	
1	vs.		
19 20	NINTENDO CO., LTD. and NINTENDO		
2	Defendants.		
22	2		
2:	3 Technology Properties Limited LLC ("TP	L"), Phoenix Digital Solutions LLC ("PDS")	
24	and Patriot Scientific Corporation ("PTSC") (collectively "Plaintiffs") hereby allege for their		
 Complaint for Patent Infringement ("Complaint") against Defendants Nintendo Co Nintendo of America, Inc. (collectively "Defendants") on personal knowledge as t 		against Defendants Nintendo Co., Ltd. and	
		nts") on personal knowledge as to their own	
2	actions and on information and belief as to the actions of Defendants, as follows:		
	1 COMPLAINT FOR PATENT INFRINGEMENT		

PARTIES

Plaintiff Technology Properties Limited LLC is a California limited liability
 company with its principal place of business at 20883 Stevens Creek Blvd., Suite 100, Cupertino,
 California 95014.

2. Plaintiff Phoenix Digital Solutions LLC is a Delaware limited liability company
with its principal place of business at 20883 Stevens Creek Blvd., Suite 100, Cupertino,
7 California 95014.

8 3. Plaintiff Patriot Scientific Corporation is a Delaware limited liability company
9 with its principal place of business at 701 Palomar Airport Road, Suite 170, Carlsbad, California
10 92011.

4. On information and belief, Defendant Nintendo Co., Ltd. is a Japanese
 corporation with a principal place of business at 11-1 Kamitoba Hokotate-Cho, Minami-Ku,
 Kyoto 601-8501, Japan.

5. On information and belief, Defendant Nintendo of America, Inc. is a Washington
 corporation with a principal place of business at 4600 15th Avenue, NE, Redmond, WA 98052.

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JURISDICTION, VENUE AND INTRADISTRICT ASSIGNMENT

6. This is an action for damages and injunctive relief based on patent infringement
arising under the patent laws of the United States, Title 35 of the United States Code.

7. This Court has jurisdiction over the subject matter of this action pursuant to 28
U.S.C. § 1331 and 28 U.S.C. § 1338.

8. On information and belief, Defendants have transacted business in this District,
 contracted to supply goods or services in this Districts directly or through their agents, have
 offered for sale, sold and/or advertised their products and services in the this District, and have
 otherwise purposely availed themselves of the privileges and benefits of the laws of the State of
 California. This Court has jurisdiction over Defendants because Defendants have committed
 acts of patent infringement during the course of their business in this District.

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19.Venue is proper in this District pursuant to 28 U.S.C. § 1400(a) and (b) and 282U.S.C. § 1391.

10. This matter is an intellectual property action and is not subject to intradistrict assignment under Civil L.R. 3-2(c).

THE ASSERTED PATENTS

The '749 Patent

11. United States Patent No. 5,440,749 ("the '749 Patent), entitled "High
Performance, Low Cost Microprocessor Architecture," issued on August 8, 1995 to Charles H.
Moore and Russell H. Fish, III. A true and correct copy of the '749 Patent is attached as Exhibit
A to this Complaint.

11 12. The '749 Patent teaches a processor that fetches multiple instructions at a time, 12 and then supplies them to the CPU's instruction register in parallel during the same memory 13 cycle they are fetched. Since memory is generally slower than the CPU, being able to fetch and 14 supply more than one instruction at a time increases the number of instructions the CPU can 15 receive in a given time, and thus increases instruction bandwidth.

13. Plaintiffs TPL, PDS and PTSC collectively hold all substantial rights to the '749 Patent.

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The '890 Patent

19 14. United States Patent No. 5,530,890 ("the '890 Patent"), entitled "High
20 Performance, Low Cost Microprocessor," issued on June 25, 1996 to Charles H. Moore and
21 Russell H. Fish, III. A true and correct copy of the '890 Patent is attached as Exhibit B to this
22 Complaint.

15. The '890 Patent teaches a dual stack architecture and the use of stack pointers that
can reference memory in any location to provide more architectural flexibility and faster access
to data elements. A stack architecture is sometimes analogized to a spring-loaded stack of plates
of the kind used in a restaurant. The last plate placed (or "pushed") on the top of the stack is the
first plate removed (or "popped") off the stack when needed. Like plates, data elements can be

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"pushed" onto or "popped" off the stack. However, by using a "stack pointer," the CPU does not 1 need to be an actual top-to-bottom "spring-loaded" stack. Instead, the stack pointer keeps track 2 of where the "top of stack" item is in a "virtual stack," so it can be accessed directly as if it were 3 on the "top." Combining this with other features, such as a memory controller and direct 4 memory access, the '890 Patent allows the CPU to off-load memory transfer of data to achieve 5 further efficiencies and higher performance. 6

Plaintiffs TPL, PDS and PTSC collectively hold all substantial rights to the '890 16. 7 Patent. 8

The '336 Patent

17. United States Patent No. 5,809,336 ("the '336 Patent"), entitled "High Performance Microprocessor Having Variable Speed System Clock," was duly and legally issued on September 15, 1998 to Charles H. Moore and Russell H. Fish, III. A true and correct copy of the '336 Patent is attached as Exhibit C to this Complaint.

The '336 Patent teaches the use of two independent clocks in a microprocessor 18. system: (1) an on-chip clock to time the CPU; and (2) a second independent clock to time the input/output (I/O) interface. This innovation was widely adopted by the industry and became 16 fundamental to the increased speed and efficiency of modern microprocessors. Decoupling the 17 system clock from the I/O clock allows the clocks to run independently (or "asynchronously"). 18

Plaintiffs TPL, PDS and PTSC collectively hold all substantial rights to the '336 19. 19 Patent. 20

COUNT I

INFRINGEMENT OF THE '749 PATENT

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Plaintiffs reallege and incorporate herein by reference the allegations contained in 20. 23 paragraphs 1-19. 24

On information and belief, without a license or permission from Plaintiffs, 21. 25 Defendants have infringed and continue to infringe at least claim 1 of the '749 Patent. 26 Defendants' infringing activities in the United States and in this District include importing, 27

COMPLAINT FOR PATENT INFRINGEMENT

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making, using, offering to sell, and/or selling products and devices that embody and/or practice
the patented invention, including but not limited to the DSi.

3 22. On information and belief, Defendants' direct infringement of the '749 Patent has
4 caused and continues to cause substantial damage to Plaintiffs.

23. On information and belief, Defendants' direct infringement of the '749 Patent has been and continues to be willful and deliberate, entitling Plaintiffs to enhanced damages and attorneys' fees.

COUNT II

INFRINGEMENT OF THE '890 PATENT

24. Plaintiffs reallege and incorporate herein by reference the allegations contained in paragraphs 1-23.

25. On information and belief, without a license or permission from Plaintiffs,
Defendants have infringed and continue to infringe at least claim 7 of the '890 Patent.
Defendants' infringing activities in the United States and in this District include importing,
making, using, offering to sell, and/or selling products and devices that embody and/or practice
the patented invention, including but not limited to the DSi.

26. On information and belief, Defendants' direct infringement of the '890 Patent has
caused and continues to cause substantial damage to Plaintiffs.

27. On information and belief, Defendants' direct infringement of the '890 Patent has
been and continues to be willful and deliberate, entitling Plaintiffs to enhanced damages and
attorneys' fees.

COUNT III

INFRINGEMENT OF THE '336 PATENT

24 28. Plaintiffs reallege and incorporate herein by reference the allegations contained in
25 paragraphs 1-27.

26 29. On information and belief, without a license or permission from Plaintiffs,
27 Defendants have infringed and continue to infringe, induced others to infringe and continue to

COMPLAINT FOR PATENT INFRINGEMENT

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induce others to infringe, and/or have committed and continue to commit acts of contributory infringement, literally or under the doctrine of equivalents, at least claim 1 of the '336 Patent. Defendants' infringing activities in the United States and in this District include importing, making, using, offering to sell, and/or selling products and devices that embody and/or practice 4 the patented invention, including but not limited to the DSi, and contributing to, and inducing 5 consumers and users to make and use the patented invention and to practice the claimed 6 methods. 7

On information and belief, Defendants induce others to infringe at least claim 1 of 30. the '336 Patent by encouraging and facilitating others to perform actions known by Defendants to infringe and with the intent that performance of the actions will infringe. TPL provided Nintendo notice of the '336 Patent by letter (with an enclosed disk identifying the MMP patents) dated October 3, 2005.

On information and belief, Defendants induce consumers to make and use the 31. 13 claimed inventions and to practice the claimed methods by (i) providing the Dsi with a USB 14 input/output interface for connecting the accused device to a peripheral device, the peripheral 15 device having a clock independent of the CPU clock (e.g., ring oscillator) connected to the 16 central processing unit on the microprocessors of the DSi and (ii) instructing consumers to 17 connect the accused product to a peripheral device such that the combination includes each 18 element of the asserted apparatus claims of the '336 Patent and use of the combination, as 19 intended, practices each of the elements of at least claim 1 of the '336 Patent. 20

On information and belief, consumers make and use the claimed inventions and 32. practice the claimed methods by using the DSi in combination with a peripheral device having a 22 clock that originates clock signals from a source other than the clock connected to the central 23 processing unit on the microprocessor of the DSi, thereby directly infringing at least claim 1of 24 the '336 Patent. 25

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COMPLAINT FOR PATENT INFRINGEMENT

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1	33.	On information and belief, Defendants' direct, induced and/or contributory	
2	infringement of the '336 Patent has caused and continues to cause substantial damage to		
3	Plaintiffs.		
4	34.	On information and belief, Defendants' direct, induced and/or contributory	
5	infringement of the '336 Patent has been and continues to be willful, entitling Plaintiffs to		
6	enhanced damages and attorneys' fees.		
7		PRAYER FOR RELIEF	
8	WHEREFORE, Plaintiffs Technology Properties Limited, Phoenix Digital Solutions		
9	LLC, and Patriot Scientific Corporation pray for judgment and relief as follows:		
10	А.	A judgment that Defendants have infringed one or more claims of the Asserted	
11	Patents;		
12	B.	An award of damages in an amount adequate to compensate Plaintiffs for	
13	Defendants' infringement of the Asserted Patents;		
14	C.	A declaration that Defendants' infringement of the Asserted Patents was willful	
15	and that this case is exceptional pursuant to 35 U.S.C. § 285;		
16	D.	An award of Plaintiffs' costs, expenses and attorneys' fees incurred in bringing	
17	and prosecution	ng this action;	
18	E.	An award of enhanced damages resulting from Defendants' willful infringement,	
19	and all other categories of damages allowed by 35 U.S.C. § 284;		
20	F.	An award of pre-judgment interest; and	
21	G.	Such other and further relief as this Court may deem just and appropriate.	
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	COMPLAINT F	OR PATENT INFRINGEMENT	
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1	DEMAND FOR JURY TRIAL		
2	Pursuant to Rule 38(b) of the Federal Rules of Civil Procedure, Plaintiffs demand a jury		
3	trial as to all issues so triable.		
4	Dated: July 24, 2012	Respectfully submitted,	
5		AGILITY IP LAWALLP	
6		Vinnes the	
7		James C. Ottesoň	
8		Attoineys for Plaintiffs TECHNOLOGY PROPERTIES LIMITED LLC and PHOENIX DIGITAL SOLUTIONS LLC	
9		and PHOENIX DIGITAL SOLUTIONS LLC	
10			
11 12		KIRBY NOONAN LANCE & HOGE LLP	
12 13		/s/ Charles T. Hoge Charles T. Hoge	
14		-	
15		Attorneys for Plaintiff PATRIOT SCIENTIFIC CORPORATION	
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